

## Electronic Acknowledgement Receipt

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<b>Application Number:</b>	10577666
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	8451
<b>Title of Invention:</b>	Semiconductor Device and Voltage Regulator Using the Semiconductor Device
<b>First Named Inventor/Applicant Name:</b>	Toshihisa Nagata
<b>Customer Number:</b>	24998
<b>Filer:</b>	Mark J. Thronson/Rupal Patel
<b>Filer Authorized By:</b>	Mark J. Thronson
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<b>Application Type:</b>	U.S. National Stage under 35 USC 371

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		ids.PDF	1383606 9c09b61d53d47bf24c7a05a5c0aa4b4ea9aeb4f2	yes	20

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Transmittal Letter	1	2
	Information Disclosure Statement (IDS) Filed (SB/08)	3	3
	NPL Documents	4	9
	Foreign Reference	10	20

**Warnings:**

**Information:**

<b>Total Files Size (in bytes):</b>	1383606
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**New Applications Under 35 U.S.C. 111**

**If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.**

**National Stage of an International Application under 35 U.S.C. 371**

**If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.**

**New International Application Filed with the USPTO as a Receiving Office**

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